

<p><b>Notice of References Cited</b></p>	Application/Control No. 10/063,134	Applicant(s)/Patent Under Reexamination CHANG ET AL.	
	Examiner DuyVu n Deo	Art Unit 1765	Page 1 of 1

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	N	JP10-84052-A	03-1998	Japan	Tsutomu	H01L 21/8247
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	U	Wolf et al., Silicon Processing for the VLSI Era, Vol. 1, 1986, 1986, pg 529.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
 Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.